VI-HPS

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34th VI-HPS Tuning Workshop
EPCC Scotland, 28-30 July 2020
http://www.vi-hps.org/training/tws/tw34.html

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ARM

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BSC Barcelona Supercomputing Center Centro Nacional de Supercomputación · Lawrence Livermore National Laboratory · RWTH AACHEN UNIVERSITY · TECHNISCHE UNIVERSITÄT DRESDEN · UNIVERSITÉ DE VERSAILLES ST-QUENTIN-EN-YVELINES · Universität Stuttgart

Agenda (Tuesday)

Time	Topic	Presenter
09:00	Welcome	Henty
	Introduction to VI-HPS & overview of tools	Valensi
	Introduction to parallel performance engineering	
	Introduction to lab setup; Building and running NPB/BT-MZ	Wylie
10:30	*Break*	
11:00	BSC tracing tools	Giménez & Mercadal
12:30	*Lunch*	
14:00	Hands-on coaching to apply BSC tools to analyze codes	Giménez & Mercadal
15:30	*Break*	
16:00	TAU performance system	Shende
17:30	Review of day and schedule for remainder of workshop	
18:00	*Adjourn*	

Agenda (Wednesday)

Time	Topic	Presenter
09:00	Score-P instrumentation & measurement	Wylie & Feld
	CUBE profile explorer	
10:30	*Break*	
11:00	Score-P analysis scoring & measurement filtering	Wylie & Feld
	Scalasca automated trace analysis	
12:30	*Lunch*	
14:00	Hands-on coaching to apply Score-P/Scalasca to analyze codes	Wylie & Feld
15:30	*Break*	
16:00	Hands-on coaching to apply TAU to analyze codes	Shende
17:30	Review of day and schedule for remainder of workshop	
18:00	*Adjourn*	

Agenda (Thursday)

Time	Topic	Presenter
09:00	MAQAO x86 performance analysis tools	Oseret & Ibnamar
	MAQAO hands-on exercises	
10:30	*Break*	
11:00	Hands-on coaching to apply MAQAO to analyze codes	Oseret & Ibnamar
12:30	*Lunch*	
14:00	ARM MAP and performance reports	Duthie & Hulguin
15:30	*Break*	
16:00	Hands-on coaching to apply MAP to analyze codes	Duthie & Hulguin
17:30	Review of workshop	
18:00	*Adjourn*	