Review

Michael Gerndt
Technische Universität München

Summary

You've been introduced to a variety of tools

- with hints to apply and use the tools effectively

Tools provide complementary capabilities

- computational kernel & processor analyses
- communication/synchronization analyses
- load-balance, scheduling, scaling, …

Tools are designed with various trade-offs

- general-purpose versus specialized
- platform-specific versus agnostic
- simple/basic versus complex/powerful

Tool selection

Which tools you use and when you use them likely to depend on the situation

- which are available on (or for) your computer system
- which support your programming paradigms and languages
- which you are familiar (comfortable) with using
- which type of issue you suspect
- which question you want to have answered

Being aware of (potentially) available tools and their capabilities can help finding the most appropriate tools

Workflow (getting started)

First ensure that the parallel application runs correctly

- no-one will care how quickly you can get invalid answers or produce a set of corefiles
- parallel debuggers help isolate known problems
- correctness checking tools like MUST identify other issues
- (that might not cause problems right now, but will eventually)
  - e.g., race conditions, invalid/non-compliant usage

Best to start with an overview of execution performance

- fraction of time spent in computation vs communication/synchronization vs I/O
- which sections of the application/library code are most costly
- Example profiler: Score-P/CUBE

and how it changes with scale or different configurations

- processes vs threads, mappings, bindings (Periscope)

Workflow (communication / synchronization)

Communication issues generally apply to every computer system (to different extents) and grow with the number of processes / threads

- Weak scaling: fixed computation per thread, and perhaps fixed localities, but increasingly distributed
- Strong scaling: constant total computation, increasingly divided amongst threads, while communication grows
- Collective communication (particularly of type “all-to-all”) result in increasing data movement
- Synchronizations of larger groups are increasingly costly
- Load-balancing becomes increasingly challenging; imbalances increasingly expensive
  - generally manifests as waiting time at following collective operations

Workflow (wasted waiting time)

Waiting times are difficult to determine in basic profiles

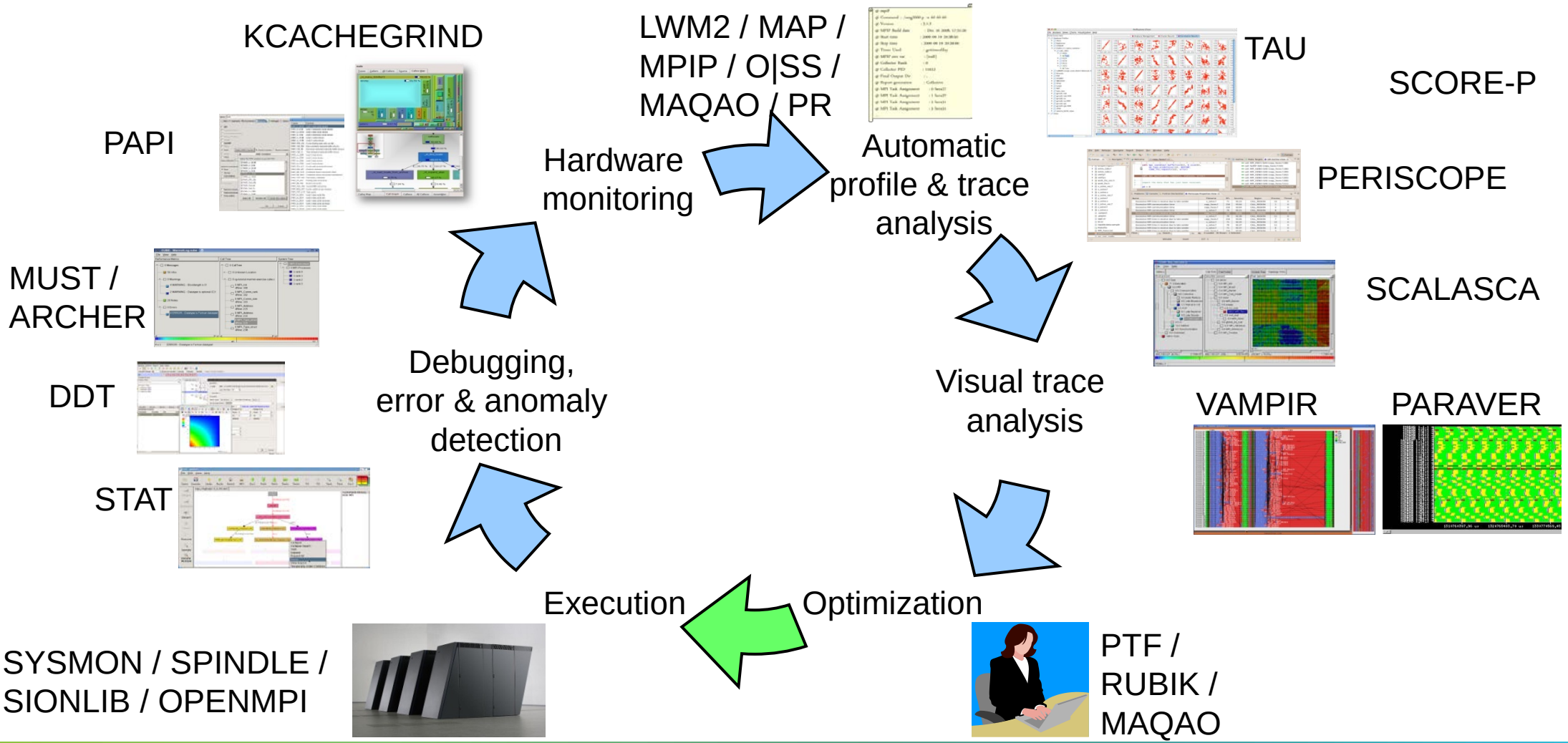
- Part of the time each process/thread spends in communication & synchronization operations may be wasted waiting time
- Need to correlate event times between processes/threads
  - **Periscope** uses augmented messages to transfer timestamps plus on-line analysis processes
  - Post-mortem event trace analysis avoids interference and provides a complete history
  - **Scalasca** automates trace analysis and ensures waiting times are completely quantified
  - **Vampir** allows interactive exploration and detailed examination of reasons for inefficiencies
  - **Paraver** offers detailed customizable analyses of execution traces

Workflow (core computation)

Effective computation within processors/cores is also vital

- Optimized libraries may already be available
- Optimizing compilers can also do a lot
  - provided the code is clearly written and not too complex
  - appropriate directives and other hints can also help
- Processor hardware counters can also provide insight
  - although hardware-specific interpretation required
- Tools available from processor and system vendors help navigate and interpret processor-specific performance issues
- MAQAO also supports binary code analysis & optimization for Intel architectures

Technologies and their integration



Evaluation / Feedback

- It's essential that you complete the on-line PRACE training event evaluation form, which is a requirement for your participation in the workshop
  - **https://events.prace-ri.eu/event/460/evaluation/**

- Please also complete and return the paper VI-HPS workshop form
  - provides valuable feedback
    - to improve future tuning workshops and training
    - to tools developers for improving their tools and training material
  - can be anonymous if desired

- Tools support queries and bug reports are also welcome
  - should be submitted to respective support mailing lists
  - and/or sometimes to local technical support staff

22nd VI-HPS Tuning Workshop (Montpellier/F, 23-27 May 2016)

- Hosted by CINES (Centre Informatique National de l'Enseignement Supérieur)
  - using ***Occigen*** Bull B710 compute nodes comprising Intel Xeon E5-2690 `Haswell' processors

- PRACE Advanced Training Centre (PATC) curriculum event
  - sponsored by Maison de la Simulation (MdS, France)
  - **no participation fee!**
  - registration deadline 02 May 2016

- Featured tools:
  - MAQAO, Score-P, Scalasca, TAU (incl. PerfExplorer), Vampir
- Further information and registration
  - **http://www.vi-hps.org/training/tws/tw22.html**

- **Inform your colleagues!**

Further information on VI-HPS website

- Introductory information about the VI-HPS portfolio of tools for high-productivity parallel application development
  - VI-HPS Tools Guide
  - links to individual tools sites for details and download
- Training material
  - tutorial slides & presentation videos
  - latest ISO/OVA image of VI-HPS Linux DVD with productivity tools
  - user guides and reference manuals for tools
- News of upcoming events
  - tutorials and workshops
  - mailing-list sign-up for announcements

http://www.vi-hps.org